

REMARKS

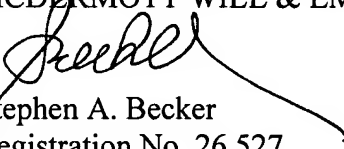
The above-referenced application has been amended to correct a minor informality with regards to the reference numbers in the specification and the drawings.

No new matter has been added.

Entry of this amendment is respectfully requested.

Respectfully submitted,

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FIG.9

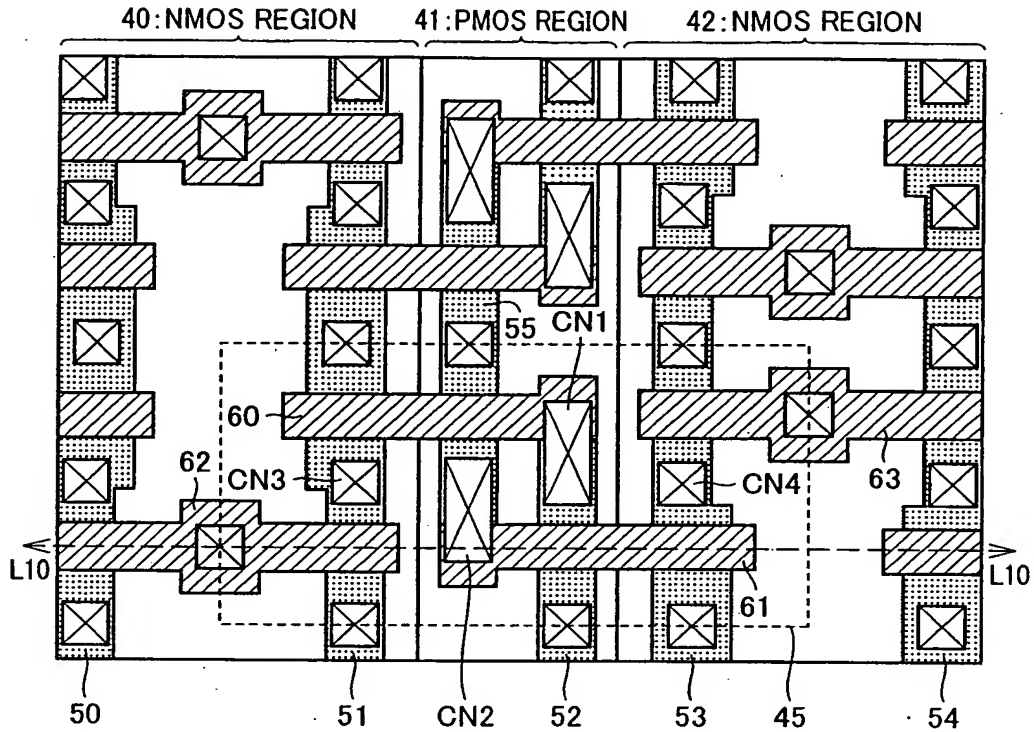
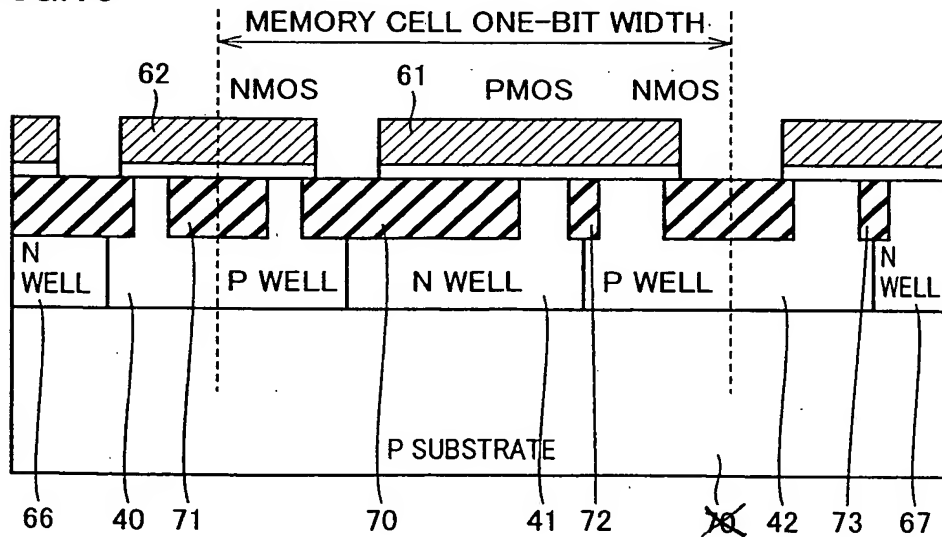


FIG.10



A cross-sectional view of a semiconductor device. The structure consists of several layers and components. At the top, there is a layer labeled 51. Below it is a layer labeled 78a. A central layer is labeled 52, with a sub-label 45. Below this is a layer labeled 53. The bottom-most layer is labeled 78c. On the left side, there are two input/output pads labeled V2a and V2b. On the right side, there are two input/output pads labeled V2c and V2d. A ground connection is labeled GND. A word line is labeled WL. A gate stack is labeled 60. A gate electrode is labeled 62. A gate insulating layer is labeled 63. A gate contact is labeled 61. A gate spacer is labeled 55. A gate opening is labeled 54. A gate trench is labeled 56. A gate etch stop layer is labeled 57. A gate etch stop layer is labeled 58. A gate etch stop layer is labeled 59. A gate etch stop layer is labeled 60. A gate etch stop layer is labeled 61. A gate etch stop layer is labeled 62. A gate etch stop layer is labeled 63. A gate etch stop layer is labeled 64. A gate etch stop layer is labeled 65. A gate etch stop layer is labeled 66. A gate etch stop layer is labeled 67. A gate etch stop layer is labeled 68. A gate etch stop layer is labeled 69. A gate etch stop layer is labeled 70. A gate etch stop layer is labeled 71. A gate etch stop layer is labeled 72. A gate etch stop layer is labeled 73. A gate etch stop layer is labeled 74. A gate etch stop layer is labeled 75. A gate etch stop layer is labeled 76. A gate etch stop layer is labeled 77. A gate etch stop layer is labeled 78. A gate etch stop layer is labeled 79. A gate etch stop layer is labeled 80. A gate etch stop layer is labeled 81. A gate etch stop layer is labeled 82. A gate etch stop layer is labeled 83. A gate etch stop layer is labeled 84. A gate etch stop layer is labeled 85. A gate etch stop layer is labeled 86. A gate etch stop layer is labeled 87. A gate etch stop layer is labeled 88. A gate etch stop layer is labeled 89. A gate etch stop layer is labeled 90. A gate etch stop layer is labeled 91. A gate etch stop layer is labeled 92. A gate etch stop layer is labeled 93. A gate etch stop layer is labeled 94. A gate etch stop layer is labeled 95. A gate etch stop layer is labeled 96. A gate etch stop layer is labeled 97. A gate etch stop layer is labeled 98. A gate etch stop layer is labeled 99. A gate etch stop layer is labeled 100.